

Learning the Portable Stimulus Standard

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On behalf of the Accellera Systems Initiative

and the Accellera Portable Stimulus Working Group



Why PSS?

 Session 01

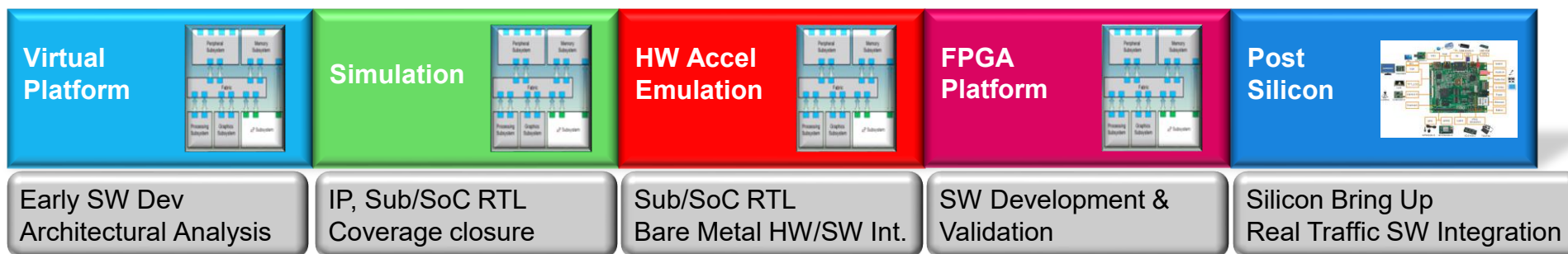
What You Will Learn

- ▶ SoC Verification Challenges
- ▶ Verification Roles
- ▶ Strengths and Weaknesses of UVM and C for Verification Testing
- ▶ Why PSS is Needed

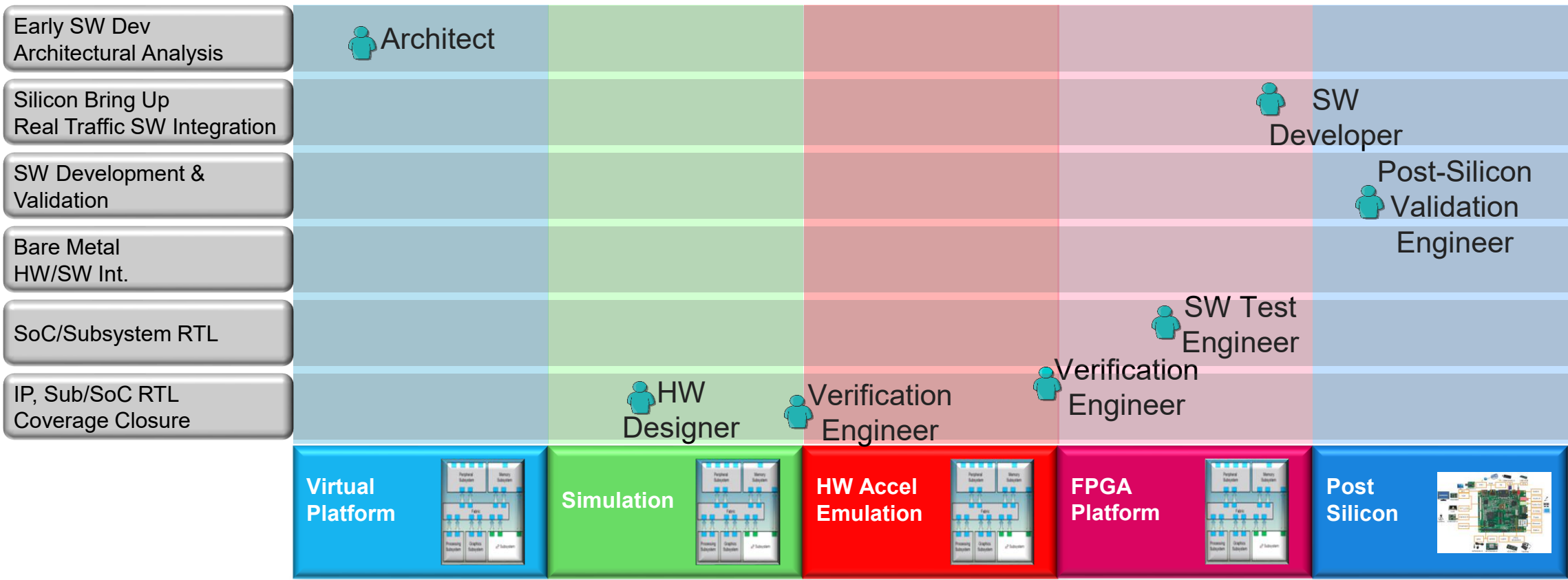


The Need for Portable Stimulus

- ✗ *Many specialized engineering resources required*
- ✗ *Significant development effort for each environment*
- ✗ *Limited sharing of models between environments*
- ✗ *Difficult to reuse tests across environments*
- ✗ *A lot of effort to migrate between environments/projects*

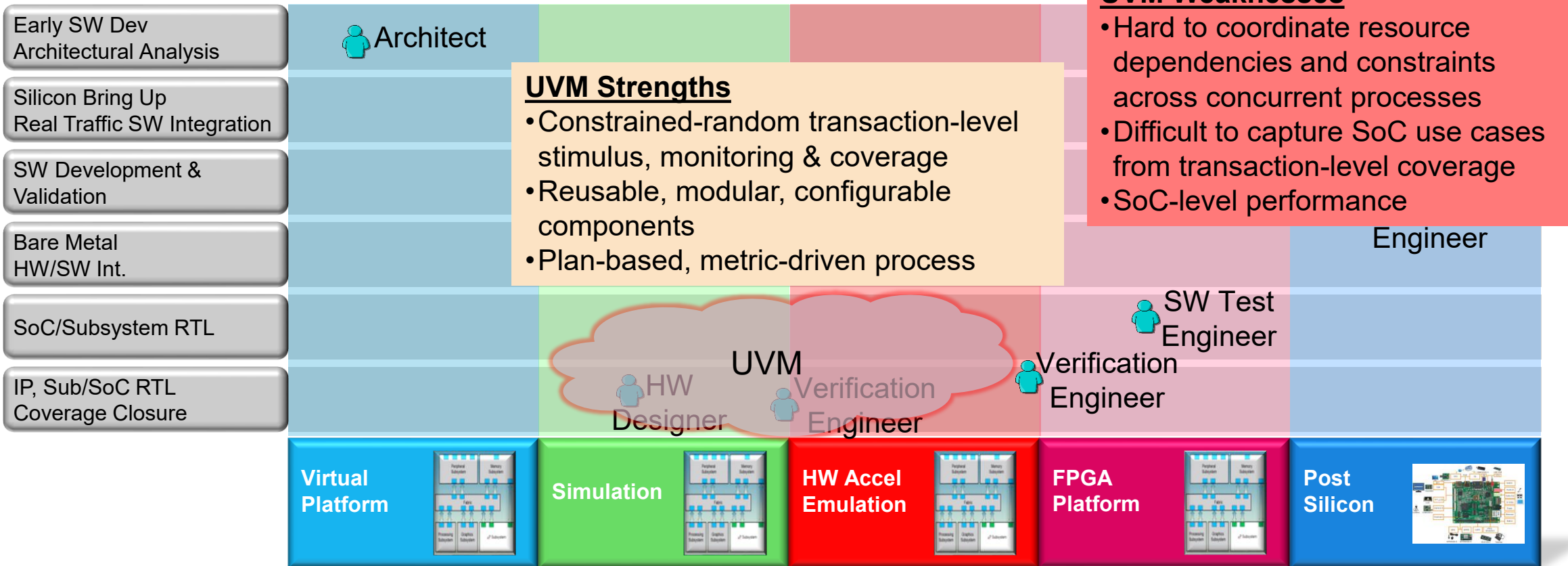


●●● The Need for Portable Stimulus

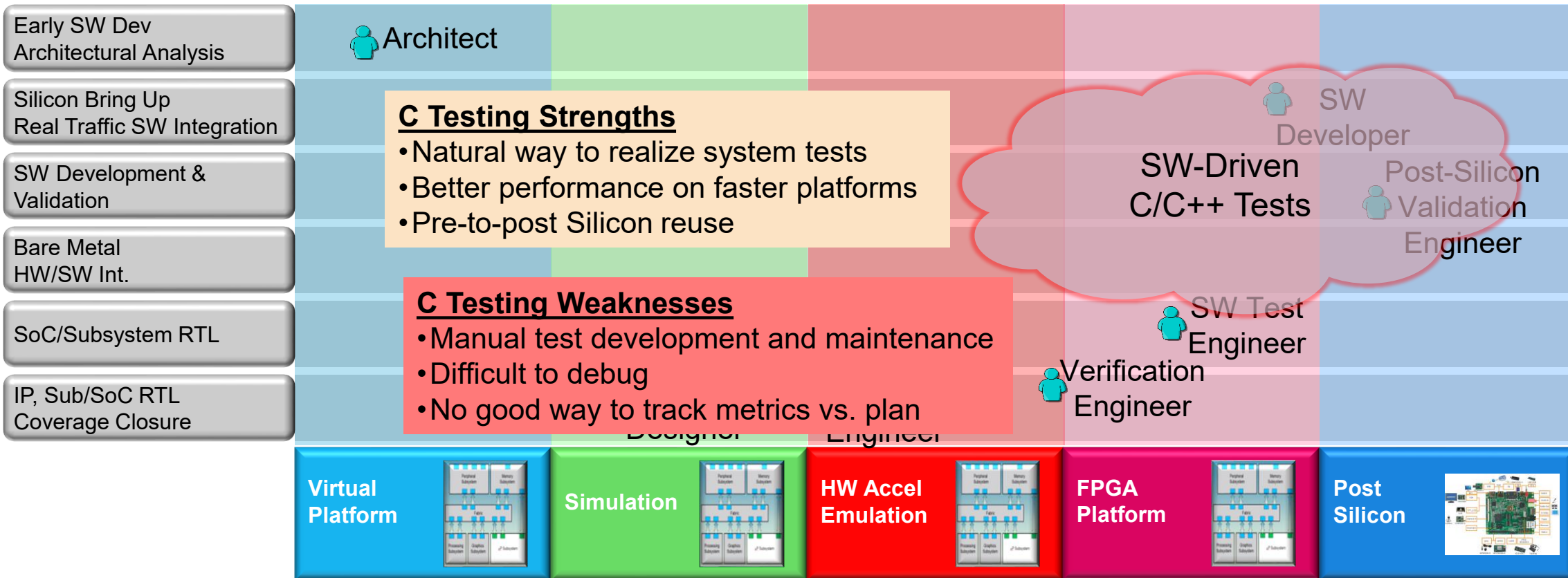




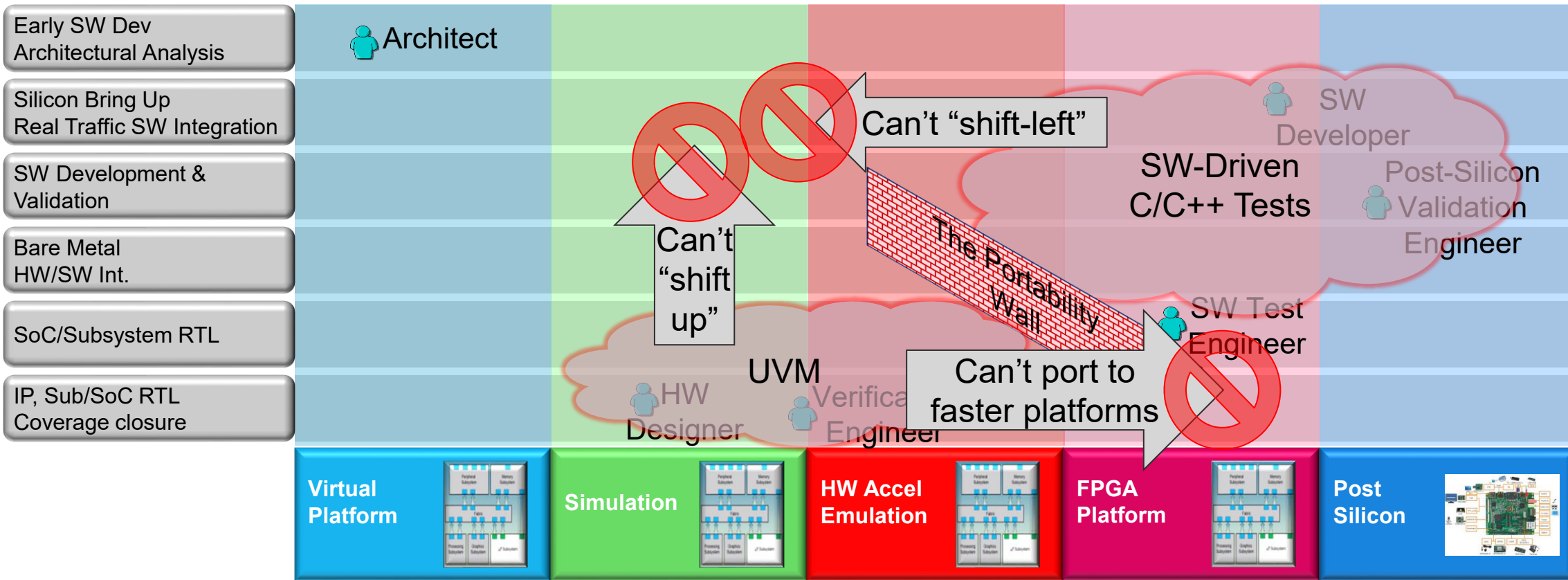
The Need for Portable Stimulus



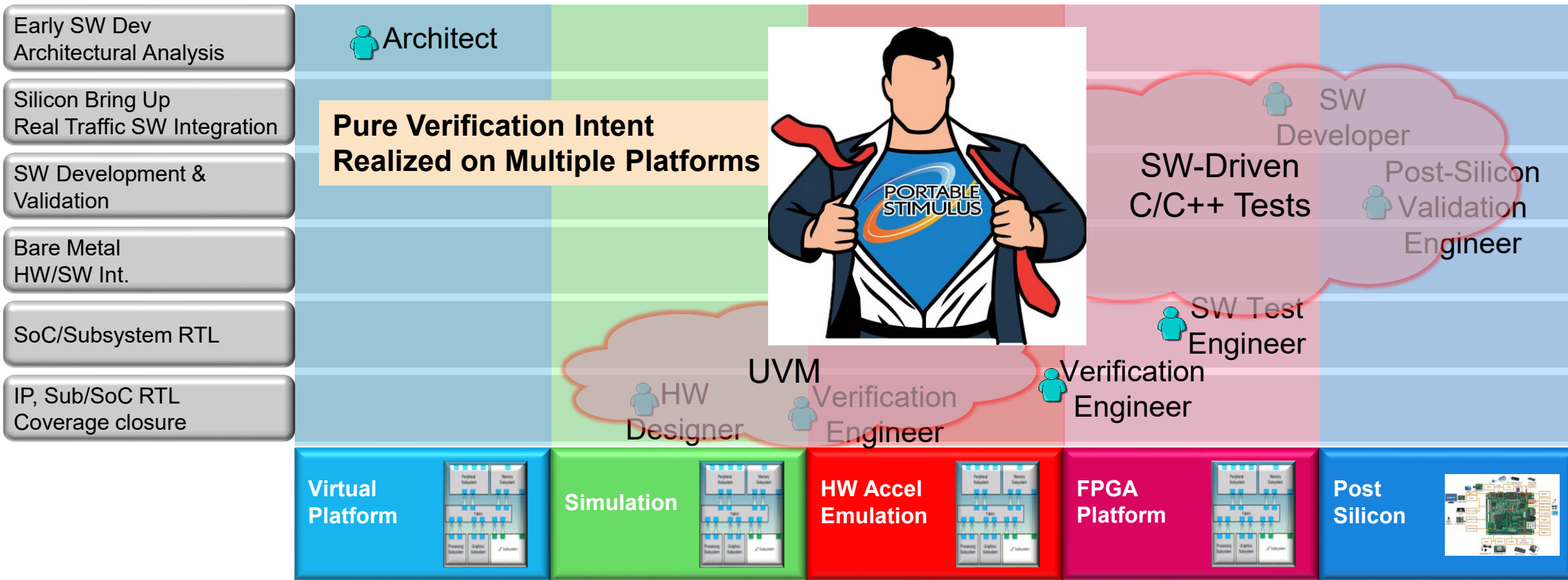
... The Need for Portable Stimulus



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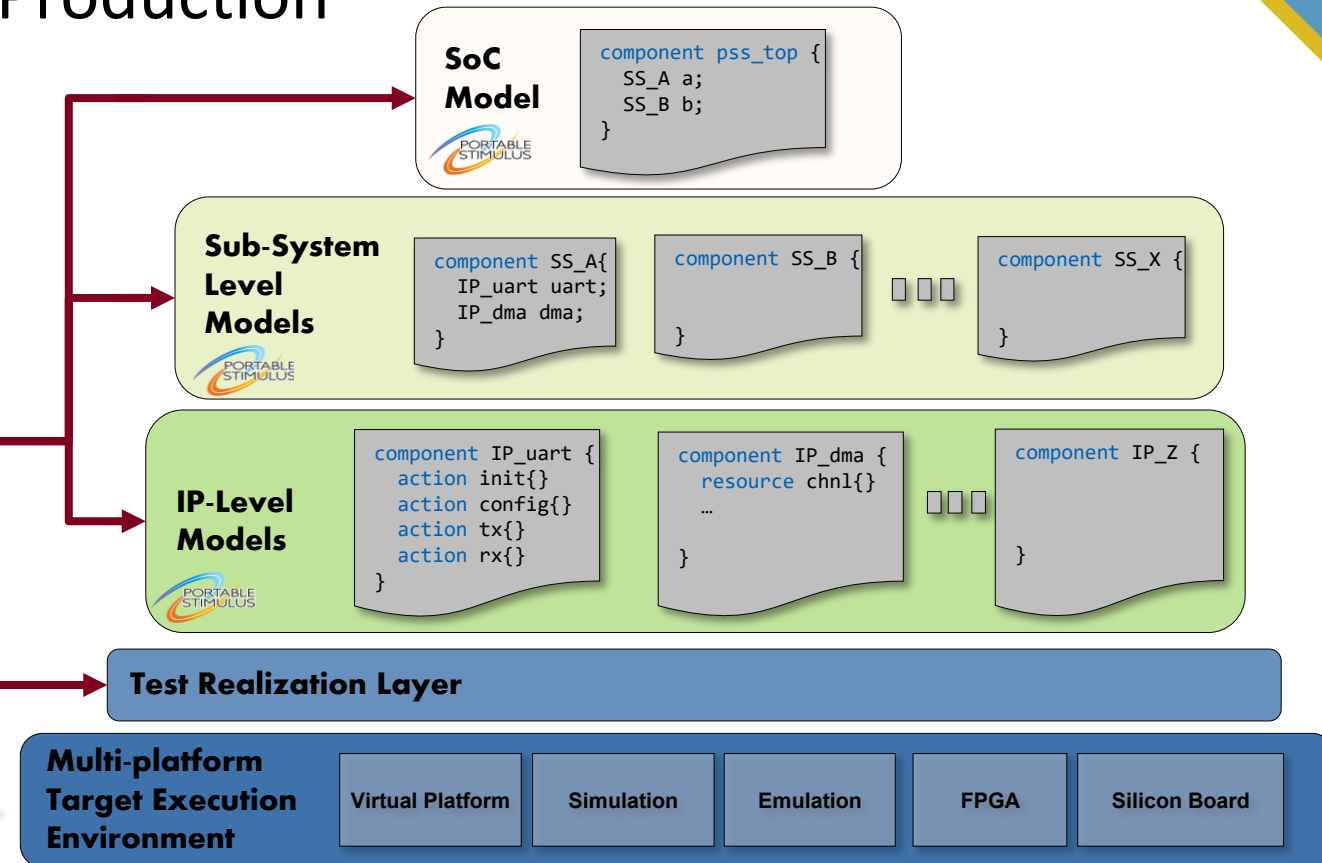
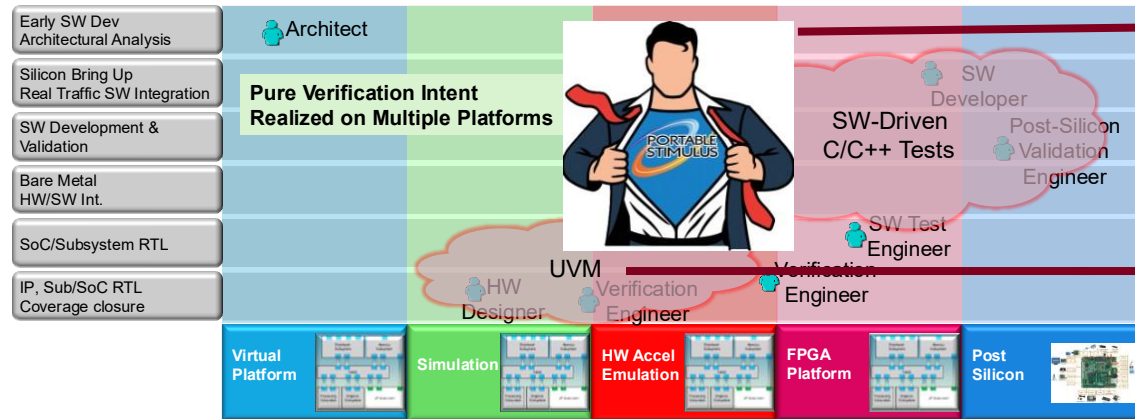


... The PSS View of a Modern SoC Design:

From Vision to Deployment and Production

Typical PSS environment:

- Hierarchy of abstract models – SoC, SubSys, IP
- Residing on top of Test Realization Layer





Thank You

