

## Insert as section 7.10.1 – In Built-in methods section

### 7.10.1 Built-in namespace

SystemVerilog provides a built-in namespace that contains types (e.g., classes), tasks and functions. The built-in namespace resides at the top of the hierarchy. Users may not insert additional declarations into the built-in namespace. The declarations in the built-in namespace are directly available in any other scope, like system tasks and functions, and can be redefined by user code in any other scope. However, unlike system tasks and functions, the tasks and functions in the built-in namespace may not be redefined by PLI functions.

<pre>built_in_data_type ::= [ :: ] data_type_identifier</pre>	<i>// Not in Annex A</i>
<pre>built_in_function_call ::= [ :: ] built_in_identifier</pre>	

The scope resolution operator `::` with no identifier on the left can be used to unambiguously access names in the global namespace. For example:

```
::sys_task();           // unambiguously call the system provided sys_task
```

Unlike system tasks and functions, tasks and functions in the global namespace need not be prefixed with a `$` to avoid collisions with user-defined identifiers. This mechanism allows functional extensions to the language in a backward compatible manner, without the addition of new keywords or polluting local name spaces.