

Advisory: Press and DAC attendees

Accellera Adds SystemVerilog Luncheon Event

To Its Design Automation Conference Event Calendar

Event includes news about Accellera's SystemVerilog IEEE plans & SystemVerilog products

Who

Accellera, the electronics industry organization focused on electronic design automation (EDA) standards, invites the electronic design community to attend its SystemVerilog luncheon event and raffle at the Design Automation Conference (DAC) June 8th in San Diego.

What

Accellera's SystemVerilog: Right Here! Right Now! luncheon event is sponsored by Mentor Graphics and Synopsys. All attendees are eligible to enter an Apple iPod drawing, and the first 300 attendees receive 50% off the price of Doulos' new "SystemVerilog 3.1a Golden Reference Guide."

Overview

See SystemVerilog in action, Right here! Right now! Over 30 EDA companies, training providers, and IP vendors are deploying SystemVerilog language support in their products today. Join your host Gabe Moretti, Editor of EDN, at this fast-paced, first of its kind, timely event. Learn how Accellera is transferring the SystemVerilog language to the IEEE Standards Association. Learn what's new in SystemVerilog 3.1a. See how industry-leading engineering teams are using SystemVerilog to create state-of-the-art system-on-chip designs. And see how many suppliers can declare their support for SystemVerilog in 60 minutes.

When

Doors open and lunch begins at 11:45 a.m.

Program

Agenda

11:45a.m.	Registration and Lunch
12:15p.m.	Welcome: Gabe Moretti, Technical Editor, EDA and ASICs, EDN
12:20p.m.	SystemVerilog Right Here! The Handoff to the IEEE Dennis Brophy, Chairman, Accellera Edward Rashba, Manager, New Technical Programs, IEEE Standards Association
12:30p.m.	SystemVerilog 3.1a Unwrapped: Vassilios Gerousis, Chairman, Accellera Technical Coordination Committee
12:40p.m.	SystemVerilog in Action: A User's Perspective Matt Maidment, Sr. CAD Engineer, Intel Corporation
12:50p.m.	SystemVerilog Right Now! Rapid fire support announcements from SystemVerilog suppliers
1:50p.m.	Closing & Prize Drawing: Gabe Moretti

Where

San Diego Convention Center, Room 29

Information and Registration:

For more information about Accellera at DAC and to register for the Accellera events at DAC, please visit www.accellera.org or www.accellera.org/calendar.html, respectively.

Accellera's breakfasts, luncheon and member meeting are free and open to all DAC attendees.

For more information about DAC, visit www.dac.com.

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About Accellera

Accellera provides design standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control. For more information about Accellera, please visit www.accelera.org.

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