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# IEEE Approves Four Accellera and SPIRIT Consortium Standards in 2009

*Total sets one-year ratification record*

Contacts:

Karen McCabe, IEEE-SA Marketing Director  
+1 732-562-3824, [k.mccabe@ieee.org](mailto:k.mccabe@ieee.org)

Georgia Marszalek, ValleyPR LLC for Accellera  
+1650-345-7477, [Georgia@ValleyPR.com](mailto:Georgia@ValleyPR.com)

**PISCATAWAY, N.J., and NAPA, California, USA**, 16 February 2010 -- The [IEEE](#), [Accellera](#) and [The SPIRIT Consortium](#), announced today that the IEEE has approved a record number of Accellera and The SPIRIT Consortium Electronic Design Automation (EDA) and Intellectual Property (IP) standards in 2009.

The recently approved IEEE standards are:

- Accellera's SystemVerilog standard, ratified in November as [IEEE 1800™-2009](#), the SystemVerilog-Unified Hardware Design, Specification, and Verification Language Standard
- The SPIRIT Consortium's IP-XACT standard, ratified in December as **IEEE 1685™ IP-XACT-2009**, a Standard Structure for Packaging, Integrating and Re-Using IP within Tool Flows.

They join Accellera standards approved by the IEEE earlier in 2009:

- [IEEE 1801™](#), a Standard for Design and Verification of Low Power Integrated Circuits and
- [IEEE 1450.6.1™](#), a Standard for Describing On-Chip Scan Compression.

IEEE standardization benefits semiconductor and the electronics industry by providing, improving and distributing the Electronic Design Automation (EDA) standards that increase the productivity of engineers in the worldwide design community.

For information about how to obtain copies of these IEEE standards, please visit [http://www.accellera.org/activities/ieee\\_activities/](http://www.accellera.org/activities/ieee_activities/).

"The approval of these four standards by the IEEE this year was a record-breaking achievement for our technical team and members," said Shrenik Mehta, chairman of Accellera. "We will continue to work with the IEEE and other standards' organizations to deliver industry-relevant standards for the electronics industry."

“The ratification of the IP-XACT specification as IEEE 1685 is a nice closing action as The SPIRIT Consortium moves towards completing its merger with Accellera. Many people have worked hard to get this successfully approved,” said Ralph von Vignau, president of The SPIRIT Consortium. “These new IEEE EDA standards underscore the common drive behind our organizations and lay the foundation for a bright and valuable future.”

“Working in collaboration with [Accellera](#) and The [SPIRIT Consortium](#) has resulted in a valuable and growing collection of standards for the electronic design automation industry under the IEEE Standards program,” said Judith Gorman, Managing Director, IEEE-SA. “We look forward to continuing our work with Accellera so we can collectively advance ongoing innovations in the EDA industry.”

### **About the 2009 IEEE Standards**

IEEE 1685 IP-XACT provides for a machine readable XML structure for IP modules and systems databooks. The XML data documents many different aspects of electronic design elements, enabling designers using IP-XACT tools to automatically create many different expressions of a design in a consistent and correlated way. Design and verification engineers benefit from using this IP-XACT from the automation of testbench creation and exploration.

[IEEE 1800](#), the SystemVerilog-Unified Hardware Design, Specification, and Verification Language Standard, unifies Verilog and SystemVerilog into a single language supporting multiple levels of electronic design abstraction for modeling and verification.

[IEEE 1801](#) Standard for Design and Verification of Low Power Integrated Circuits (aka SystemVerilog) provides for portability of low-power design specifications that can be used with a variety of commercial products throughout an electronic system design, analysis, verification and implementation flow

IEEE 1450.6.1 Standard for Describing On-Chip Scan Compression (aka the Open Compression Interface standard or OCI) standardizes the interface between different suppliers' tools to enable vendor interoperability for test pattern generation and diagnosis.

### **Accellera’s and The SPIRIT Consortium’s IEEE Standards**

- SystemVerilog or IEEE Std.1800
- Unified Power Format (UPF) or IEEE Std. 1801
- Open Compression Interface (OCI) or IEEE Std. 1450.6.1
- IP-XACT or IEEE Std. 1685
- VHDL or IEEE Std.1076™
- Verilog or IEEE Std. 1364™
- Property Specification Language (PSL) or IEEE Std.1850™
- Standard Delay Format (SDF) or IEEE Std. 1497™
- Delay and Power Calculation System (DPCS) or IEEE Std. 1481™
- Advanced Library Format (ALF) or IEEE Std. 1603™
- Open Model Interface (OMI) or IEEE Std. 1499™
- Standard Delay Format (SDF) or IEEE Std. 1497™

## About The SPIRIT Consortium

The SPIRIT Consortium is a global organization focused on establishing multi-faceted IP/tool integration standards that drive sustainable growth in electronic design. It is comprised of leading EDA, IP, system integration and semiconductor companies dedicated to the adoption of a unified set of specifications for configuring, integrating, and verifying IP in advanced SoC design tool sets. For more information, please visit [www.spiritconsortium.org](http://www.spiritconsortium.org).

## About Accellera

[Accellera](http://www.accelera.com), an industry organization formed in 2000, provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are transferred to the IEEE standards body for formalization and ongoing change control.

On June 11, 2009, EDA industry organizations, [Accellera](http://www.accelera.com) and The [SPIRIT Consortium](http://www.spiritconsortium.org), announced that the organizations' Boards agreed to [a merger of the two entities](#). The union improves the development of language-based and IP standards. Both organizations are aligned on the path to formalize standards through the IEEE. For more information, please visit [www.accelera.org](http://www.accelera.org).

## About the IEEE Standards Association

The IEEE Standards Association, a globally recognized standards-setting body, develops consensus standards through an open process that engages industry and brings together a broad stakeholder community. IEEE standards set specifications and best practices based on current scientific and technological knowledge. The IEEE-SA has a portfolio of 900 active standards and more than 400 standards under development. For information on the IEEE-SA, see: <http://standards.ieee.org>.

## About the IEEE

The IEEE (Institute of Electrical and Electronics Engineers, Inc.) is the world's largest technical professional society. Through its more than 375,000 members in 160 countries, the organization is a leading authority on a wide variety of areas ranging from aerospace systems, computers and telecommunications to biomedical engineering, electric power and consumer electronics. Dedicated to the advancement of technology, the IEEE publishes 30 percent of the world's literature in the electrical and electronics engineering and computer science fields, and has developed nearly 900 active industry standards. The organization annually sponsors more than 850 conferences worldwide. Additional information about the IEEE can be found at <http://www.ieee.org>.

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