



Accellera at DVCon: Learn about EDA and IP Standards, Celebrate 10-Year Anniversary, Honor 7th Technical Excellence Award Recipient

February 23-24, Doubletree Hotel, San Jose

Who/What

At Accellera's Design Verification Conference (DVCon), on Tuesday, February 23, Accellera will offer a Verification Intellectual Property (VIP) Tutorial and host a luncheon with an Electronic Design Automation (EDA) and Intellectual Property (IP) standards update and celebration of its 10-year anniversary. On Wednesday, February 24, Accellera will present its 7th annual Technical Excellence Award before DVCon's Industry Leaders Panel, *What Keeps You Up at Night?*

What:

Tutorial: Experience with VIP Interoperability Best Practices

The speakers will introduce Accellera's VIP Recommended Practices Guide and share their experience of using the recommendations on real projects – what worked, what came up short and their insights into making it better.

Speakers: Thomas Alsop - *Intel Corp.*
Janick Bergeron - *Synopsys, Inc.*
Edgar Jimenez - *Freescale Semiconductors, Inc.*
Dhrubajyoti Kalita - *Intel Corp.*
Sharon Rosenberg - *Cadence Design Systems, Inc.*

Luncheon: Accellera Standards Update & 10-year Anniversary Celebration

After Accellera's VIP Tutorial, join Accellera to celebrate 10 years of standards leadership and learn about current standards' development activities.

Speakers: Shrenik Mehta – *Accellera Chair*
Karen Pieper – *Accellera Technical Coordinating Committee (TCC) Chair*

Technical Excellence Award Presentation

Accellera's annual Award recognizes the outstanding achievements of a Technical Subcommittee (TSC) member.

When/Where

VIP Tutorial: Tuesday, February 23, 8:30am - 12:00pm | Fir
Accellera Luncheon: Tuesday, February 23, 12:00pm - 1:30pm | Pine/Cedar
Award Presentation: Wednesday, February 24, 3:00pm | Oak/Fir
DoubleTree Hotel, San Jose, California

Information

For more information about Accellera and its EDA and IP standards please visit www.accellera.org.
For more information about DVCon, please visit www.dvcon.org.

About Accellera

Accellera, an industry organization formed in 2000, provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are transferred to the IEEE standards body for formalization and ongoing change control. For more information, please visit www.accellera.org.

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