



Accellera Invites Electronic Design Community to Attend Free and Open Tutorial on the Design and Verification of Low Power SoCs at ISQED

March 18, Doubletree Hotel, San Jose, California

Who/What

[Accellera](#), the electronics industry organization focused on Electronic Design Automation (EDA) standards, announces its [tutorial session](#) on the Design and Verification of Low Power Systems-on-Chip (SoCs) at the 10th annual International Society of Quality Electronic Design ([ISQED](#)) event. The Power session is organized by Accellera officer, Yatin Trivedi of Synopsys. The session's speakers include Yatin Trivedi, John Biggs of ARM and Gary Delp of LSI.

When and Where

ISQED 2009,
Wednesday, March 18, 2009
1:30pm - 3:30pm
Doubletree Hotel, San Jose

Tutorial Abstract

Power management is critical in IC design, especially for mobile devices, battery-operated systems, and non-portable systems with power consumption constraints. However, not all applications have the same requirements for addressing power management.

In this tutorial, experts from different application areas will present the requirements and solutions for power management, including design, verification and analysis. The areas chosen for this tutorial include storage, automotive, Intellectual Property (IP) and EDA flows. A general overview of low power design and verification challenges for SoC designers will set the context for each of the application areas.

Registration Information

The Accellera session is free and open to all ISQED attendees with a Conference or free Exhibits pass. For more details about the tutorial, a complete list of ISQED'09 programs and to register please visit <http://www.isqed.org>.

For more information about Accellera and Accellera standards, please visit www.accelera.org.

About Accellera

[Accellera](http://www.accelera.org) provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control.

For more information about Accellera, please visit www.accelera.org.

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