



Accellera Invites Electronic Design Community to DATE 09 to Learn about Its Newest EDA Standards at Free and Open Town Hall Meeting

Tuesday, April 21, Acropolis, Nice, France

Who/What

[Accellera](#), the electronics industry organization focused on Electronic Design Automation (EDA) standards, will be at the Design, Automation and Test in Europe (DATE 09) event, and will hold an open and free Town Hall meeting with a presentation about its roadmap and an overview about its most recent standards, including [IEEE 1801](#)TM, the newest IEEE standard for the design and verification of low power integrated circuits. The IEEE 1801 standard is based on and is an enhancement of Accellera's Unified Power Format (UPF) 1.0 standard.

Shrenik Mehta, Accellera's Chair, will be the speaker.

When and Where

6:30 - 7:30pm, Tuesday, April 21, 2009

Acropolis, Room Gallieni 3

Nice, France

Registration Information

The Accellera Town Hall Meeting, *Accellera Standards - What's New*, is free and open to all.

For more details about [DATE 09](#), please visit www.date-conference.com.

For more information about Accellera and Accellera standards, please visit www.accellera.org.

About Accellera

[Accellera](#) provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control.

For more information about Accellera, please visit www.accellera.org.

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Press Contact: Georgia Marszalek, ValleyPR for Accellera, + (650) 345-7477, Georgia@ValleyPR.com
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