



Accellera Invites EDA Community to Its Free and Open Standards Events at the Design Automation Conference in San Francisco, Tuesday, July 28, 2009

Who/What

[Accellera](#), the electronics industry organization focused on Electronic Design Automation (EDA) standards, will be at the Design Automation Conference (DAC09) in San Francisco and will host and co-host four free and open events on Tuesday, July 28, 2009. These events include an Accellera and SpringSoft-sponsored breakfast and panel titled "Frontiers in Verification: Coverage, Closure and Beyond," technical sessions addressing the IEEE 1801 standard for low-power design and the proposed 2009 SystemVerilog standard, as well as an event, co-hosted by [Accellera](#) and The [SPIRIT Consortium](#) that celebrates and covers details about the organizations' [recently announced merger plans](#) and is sponsored by Cadence Design Systems, Denali and Synopsys.

What, When and Where

Tuesday, July 28, 2009, Moscone Convention Center

7:00am - 8:30am

Breakfast and Panel Discussion: "Frontiers in Verification: Coverage, Closure and Beyond"

This panel brings together a group of verification professionals for a lively conversation on how verification must change in the coming years.

Room 309

Moderator: Scott Sandler, vice president of corporate marketing, SpringSoft

Panelists:

Kathy Harrington, AMD

Alon Gluska, Intel

Scott Runner, Qualcomm

Avi Ziv, IBM

12:00pm - 1:30pm

IEEE 1801: Celebrating Success of Low Power User Community with UPF

Room 309

This session includes presentations from users on their experiences with using UPF: where they saw benefits, some of the future challenges, and how the standard needs to grow in order to continue driving the methodology of tomorrow's low power devices. A boxed lunch will be available for registered attendees.

1:30pm - 2:30pm

SystemVerilog Is Getting Even Better! An Update on the Proposed 2009 SystemVerilog Standard,
Room 309

This presentation provides an overview of more than 50 major new features being added in the proposed SystemVerilog-2009 standard. The presenters are Cliff Cummings of Sunburst Design and Stuart Sutherland of Sutherland HDL.

6:00pm - 7:30pm

Room 114

[The SPIRIT Consortium](#) and [Accellera](#): Learn more about the recently-announced merger and celebrate the future of IC design, Intellectual Property (IP) and verification.

Registration Information

To register, please visit <http://www.accellera.org/events/>

For more details about DAC09, please visit www.dac.com.

About Accellera

[Accellera](#) provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control. For more information about Accellera, please visit www.accellera.org.

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Press Contact:

Georgia Marszalek, ValleyPR LLC, +1650-345-7477, Georgia@ValleyPR.com

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