



## **EDA Standards Organization Accellera Aligns Intellectual Property Rights Policy with IEEE**

*New Policy Supports Accellera's Goal of Passing Its Approved EDA Standards on to the IEEE for Approval*

NAPA, CA, USA, July 22, 2009 – [Accellera](#), the leading standards organization developing language-based standards used by system, semiconductor, Intellectual Property (IP) and Electronic Design Automation (EDA) companies, announced today that it has approved an [Intellectual Property Rights](#) (IPR) policy that is aligned with the IEEE's IPR policy.

Accellera's IPR policy supports the organization's goal of forwarding its approved EDA standards to the IEEE for ratification distribution and future revision. For more information about the policy, please visit <http://www.accellera.org/governing/>.

“Our members requested that Accellera align its IPR policy with the IEEE, since their legal teams were familiar with the IEEE policy,” added Shrenik Mehta, Accellera chair. “It makes it easier for companies to join Accellera, participate and contribute to our EDA industry standards, since our policy now follows an industry-accepted IEEE policy that governs the call for potential essential patent claims, meeting guidelines and letters of assurance.”

“Whenever leading industry standards organizations like Accellera support our policies, the IEEE is able to facilitate the ratification, distribution and further revision of standards that benefit the electronics industry, increase the productivity of engineers and improve the design of electronics,” added Judith Gorman, Managing Director, IEEE Standards Association

To date, 10 Accellera EDA standards have been ratified by the IEEE:

- Verilog or IEEE 1364;
- VHDL or IEEE1076;
- Property Specification Language (PSL) or IEEE 1850;
- SystemVerilog or IEEE 1800;
- Standard Delay Format (SDF) or IEEE 1497;
- Delay and Power Calculation System (DPCS) or IEEE 1481;
- Advanced Library Format (ALF) or IEEE 1603;

- Open Compression Interface (OCI) or IEEE 1450;
- Unified Power Format (UPF) or IEEE P1801;
- Open Model Interface (OMI) or IEEE 1499.

Accellera's current standardization subcommittees include Interface Technical Committee (ITC), Open Verification Library (OVL), Unified Coverage Interoperability Standard (UCIS), Verilog Analog/Mixed Signal (A/MS) and Verification IP (VIP).

On June 11, 2009, EDA industry organizations, [Accellera](#) and The [SPIRIT Consortium](#), announced that the organizations' Boards agreed to [a merger of the two entities](#). The union improves the development of language-based and IP standards. Both organizations are aligned on the path to formalize standards through the IEEE. Further details of the merger will be announced at the Design Automation Conference (DAC) in San Francisco, Tuesday, July 28, 2009, 6:00pm - 7:30pm, Moscone Convention Center, Room 114. To register, please visit <http://www.accellera.org/events/>.

### **About Accellera**

[Accellera](#) provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control. For more information about Accellera, please visit [www.accellera.org](http://www.accellera.org).

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