



Accellera Invites EDA Community to Attend Its Luncheon, OVL Survey and Technical Excellence Award Presentations at DVCon

February 24-26, 2009, Doubletree Hotel, San Jose, California

San Jose, Calif., February 19, 2009 — [Accellera](#), the electronics industry organization focused on Electronic Design Automation (EDA) standards, today announced a series of Accellera-related events at its Design and Verification Conference and Exhibition ([DVCon](#)), at the Doubletree hotel in San Jose, California. At noon on Tuesday, February 24, 2009 Accellera will host a luncheon and standards presentation that covers results from its recent [Open Verification Library \(OVL\) survey](#). At 3:15pm on Wednesday, February 25th, before DVCon's free [EDA: Dead or Alive](#) panel, Accellera will present its 2009 [Technical Excellence Award](#).

“We chose DVCon as the ideal venue for getting the word out about our standards and to present our Technical Excellence Award to one of the most significant contributors from our Technical Subcommittees,” said Shrenik Mehta, Accellera chair.

[Registration](#) for DVCon exhibits is complimentary and includes free access to several conference events, including the Wednesday, February 25th Keynote at 2pm and the Wednesday Panel at 3:15pm.

The Tuesday, February 24th Accellera luncheon and presentations are open to all registered DVCon tutorial participants. The Wednesday, February 25th Accellera Technical Excellence Award presentation before DVCon's [EDA: Dead or Alive](#) panel is free and open to all registered attendees (exhibits, tutorial and conference). To register, please visit www.dvcon.org.

Accellera's standards luncheon presentation begins at noon on Tuesday. The topics and speakers are:

- Welcome and Review of Accellera Accomplishments and Goals, Shrenik Mehta, Accellera Chair
- Overview of Technical Subcommittee (TSC) Activities, Karen Pieper, Accellera Technical Committee Chair
- Unified Coverage Interoperability Standard (UCIS), Richard Ho, TSC Chair
- IEEE P1801 Unified Power Format (UPF) Standard, Stephen Bailey, Working Group Chair
- OVL Survey Results, Kenneth Larsen, TSC Chair
- Q & A
- Call for Participation in Accellera's TSCs, Karen Pieper

About Accellera's Technical Excellence Award

Each year, Accellera's Technical Excellence Award recognizes the outstanding achievements of a Technical Subcommittee member. Candidates are nominated by the industry at large, and nominations are endorsed by participants in Accellera's Technical Subcommittees. All Accellera Technical Subcommittee members are eligible for the award.

About Accellera's Electronic Design Standards and Technical Subcommittees

[Accellera](#) provides a framework that enables the technical work of its committees, logistics and the infrastructure for obtaining and distributing funds and resources for EDA standards that, when approved, are available to everyone in the electronics community at no cost.

Accellera has transferred its completed standards work to the IEEE and continues to use this strategy as part of the roadmap for all of its standards.

[Accellera's Technical Subcommittees](#) produce effective and efficient standards for today's advanced IC designs. Participation comes from Accellera member companies, industry contributors, and independents. Technical contributors typically have many years of practical experience with IC design and developing and using design automation tools.

About Accellera's DVCon

The Design and Verification Conference and Exhibition ([DVCon](#)) is sponsored by Accellera and is a focused on the functional design and verification of electronic systems.

About Accellera

[Accellera](#) provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE Standards Association for formalization and ongoing change control. For more information about Accellera, please visit www.accellera.org.

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Notes to editors:

If you wish to attend the Accellera luncheon and presentations on Tuesday, February 24th, please confirm by sending an email to Georgia@ValleyPR.com.

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