



## **Accellera Chairman, Shrenik Mehta, Invited to Speak at Standards in EDA**

### **Session at the VLSI Conference in India**

*Tuesday, January 8, 2008, Hyderabad, India*

#### **Who**

Shrenik Mehta, Accellera's chairman, has been invited to speak during the Standards in EDA session at the VLSI Conference in Bangalore, India.

Accellera is the Electronic Design Automation (EDA) organization focused on design standards that increase electronic design productivity.

#### **What**

Standards in EDA (Session D5), Organized by Nagi Naganathan and Chaired by Apurva Kalia  
Shrenik Mehta's talk covers several different Accellera standards and explains how they fit into a design flow. It includes updates on the status of the Open Compression Interface (OCI), Unified Power Format (UPF), Unified Coverage Interoperability (UCIS) and Open Verification Library (OVL) standards.

Accellera's Verilog-AMS (Analog Mixed-Signal) standard will be covered by Accellera Technical Committee member, Srikanth Chandrasekaran of Freescale.

#### **When and Where**

11.00 am to 1.00 pm, Tuesday, January 8, 2008

HICC, Hyderabad, India

#### **Information**

For more information on the VLSI Conference, please visit [www.vlsiconference.com](http://www.vlsiconference.com).

For more information about Accellera and Accellera standards, please visit [www.accelera.org](http://www.accelera.org)

#### **About Accellera**

Accellera provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA

standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control.

For more information about Accellera, please visit [www.accelera.org](http://www.accelera.org).

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