



**Mark Your Calendars, Accellera Hosts Open Session
On Newest Standards Developments at DATE 2008**

Wednesday, 12 March, 2008, 1230-1400

Who

At the DATE ([Design, Automation and Test in Europe](#)) conference, [Accellera](#), the electronics industry organization focused on Electronic Design Automation (EDA) standards, is holding an open industry meeting covering its [most recent standards](#), including SystemVerilog; Verilog AMS; Open Verification Library (OVL); Open Compression Interface (OCI); Assertions, SystemVerilog and the Property Specification Language (PSL); Unified Power Format (UPF) and Unified Coverage Interoperability (UCI).

When and Where

Wednesday, 12 March, 2008

12:30-2:00pm

ICM, Munich Germany

Information

For more information about Accellera and Accellera standards, please visit www.accellera.org.

To register for DATE, please visit www.date-conference.com.

About Accellera

Accellera provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control.

For more information about Accellera, please visit www.accellera.org.

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