



For Release July 24, 2007

## **Accellera Announces Election of Corporate Member Companies to Its Board**

*Semiconductor, IP, Systems and EDA companies play key role  
in developing new electronic design standards*

NAPA, Calif., July 24, 2007, — Accellera, the electronics industry organization focused on Electronic Design Automation (EDA) standards, announced today that its Board of Directors elected fourteen Corporate Member companies to its Board for the 2007/8 membership year. The election was held at the Accellera Board meeting at the Design Automation Conference last month.

Accellera Corporate Member companies for 2007/8 are: ARM Ltd., Cadence Design Systems, Denali Software Inc., Freescale Semiconductor, IBM, Intel Corporation, Magma Design Automation, Mentor Graphics, Nokia, Novas, Rockwell Collins, Sun Microsystems, Synopsys Inc. and Texas Instruments. The Accellera Board, comprised of these companies, includes representation from users and suppliers alike.

"Accellera's goal is to improve electronic design productivity through open standards, while creating a level playing field for all participants," remarked Shrenik Mehta, Accellera chair. "It is advantageous that we have a balance of semiconductor, IP, systems houses and EDA companies represented on our Board."

Bringing additional industry perspective to Accellera standards are: Aldec, Inc., Archpro Design Automation, Azuro, Inc., Certess Inc., Jasper Design Automation, OneSpin Solutions, Silvaco, SpringSoft, Inc., ST Microelectronics, Toshiba and Xilinx, which are represented in Accellera as Associate Members.

Accellera has developed nine standards that have been transferred to the IEEE. Seven have been ratified by the IEEE and the two newest ones are in active IEEE working groups now. Accellera's successes in advanced design and verification language standards include SystemVerilog and the Property Specification Language (PSL). Accellera recently completed the Unified Power Format (UPF) standard and is currently developing a Unified Coverage Interoperability (UCI) standard.

### **About Accellera's Technical Subcommittees**

Accellera's Technical Subcommittees produce effective and efficient standards for today's advanced IC designs. Participation comes from Accellera member companies, industry contributors, and independents. Technical contributors typically have many years of practical experience with IC design and developing and using design automation tools.

Accellera's current Technical Subcommittees include: Interface (ITC), Open Compression Interface (OCI), Open Verification Language (OVL), Property Specification Standard (PSL), SystemVerilog, Unified Coverage Interoperability (OCI), Unified Power Format (UPF), Verilog Analog Mixed-Signal (Verilog-AMS) and VHDL. More information is at [www.accellera.org](http://www.accellera.org).

### **About Accellera**

Accellera provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control.

For more information about Accellera, please visit [www.accellera.org](http://www.accellera.org).

**-end-**

Press Contact: Georgia Marszalek, ValleyPR for Accellera, + (650) 345-7477,  
Georgia@ValleyPR.com

*All trademarks and tradenames are the property of their respective owners.*