



## **Accellera Announces Call for Nominations for Annual Technical Excellence Award Honoring Contributions to Its Design Automation Standards Efforts**

*Nominations are due Friday, April 6, 2007*

### **Who/What**

Accellera, the Electronic Design Automation (EDA) organization focused on electronic design standards, invites the electronic design community to nominate an individual for its 4th annual Technical Excellence Award. Accellera's Technical Excellence Award recognizes an outstanding individual who has helped create electronic design standards that benefit the EDA industry.

### **When/Where**

Nominations are due Friday, April 6, 2007. Please visit [www.accellera.org](http://www.accellera.org) to nominate an individual. The Award will be presented during Accellera's open meeting at the Design Automation Conference (DAC) in San Diego in June 2007.

### **About the Award**

Accellera's Technical Excellence Award recognizes the outstanding achievements of its Technical Committee members. Accellera will select a single contributor to the Accellera standards development process as its 2007 award recipient. All Accellera Technical Subcommittee members are eligible for the award. A nomination form and more information are at [www.accellera.org](http://www.accellera.org).

### **About Accellera's Technical Subcommittees**

Accellera's Technical Subcommittees include: Standard Co-Emulation Applications Programming Interface (SCE-API), Open Compression Interface (OCI), Open Verification Library (OVL), Unified Coverage Interoperability (UCI), Unified Power Format (UPF) and Verilog Analog Mixed-Signal (AMS). For more information about Accellera's Technical Subcommittees, please visit, [www.accellera.org](http://www.accellera.org).

Accellera's Technical Subcommittees produce effective and efficient standards for today's advanced semiconductor designs. Participation comes from Accellera member companies, industry contributors and independents. Technical contributors typically have many years of practical experience developing and using design automation tools.

### **Information**

For more information about Accellera and its EDA standards please visit [www.accellera.org](http://www.accellera.org).

For more information about the 44<sup>th</sup> annual DAC, please visit [www.dac.com](http://www.dac.com).

### **About Accellera**

Accellera provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control.

Accellera has developed seven standards that have been ratified by the IEEE. Accellera's recent successes in advanced design and verification language standards include SystemVerilog and the Property Specification Language (PSL). Accellera is currently developing a Unified Power Format (UPF) and Unified Coverage Interoperability (UCI) standard.

For more information about Accellera, please visit [www.accellera.org](http://www.accellera.org).

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