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Accellera Furthers Electronic Design Productivity and Interoperability through Standard Coverage Metrics

Unified Coverage Interoperability Standard Improves Verification Efficiency Measurements

NAPA, Calif., December 14, 2006, — Accellera, the electronics industry organization focused on electronic design automation (EDA) standards, announced today that its Board of Directors approved the formation of the new Unified Coverage Interoperability Technical Subcommittee (UCI TSC). The committee is charged with defining standards to enable sharing and analysis of coverage data generated by different tools during the verification process. Coverage data is typically used during verification to determine if the verification goals have been met when using different tools and methodologies. The growing complexity of designs now requires that coverage data be shared among different tools to achieve verification closure.

The UCI standard will provide a consistent way to quantify and share coverage data for today's and the next generation of EDA tools.

"Electronic designers need a common standard for measuring their verification coverage when using tools from multiple sources and suppliers," stated Shrenik Mehta, Accellera chair. "Our new committee is chartered to define a standard way to quantify the coverage metrics across software simulators, hardware accelerators, symbolic simulators and formal tools."

The goals of the UCI-TSC include:

*Defining standard coverage models for commonly used metrics,

*Identifying interoperability requirements among various coverage sources,

*Defining a standard that allows coverage data to be exchanged among different EDA suppliers' tools,

*Defining standard formats for representing coverage data,

*Encouraging user and EDA technology advancement for the next generation of coverage solutions.

The UCI standard will include terms and definitions of code and functional coverage metrics, data formats and an Application Programming Interface (API) specification.

For More Information

UCI is open for all interested parties to participate. For more information about the committee and to join, please visit: www.accellera.org/activities/ucis. The first committee meeting will be held in January 2007 at a time and place to be announced.

About Accellera

Accellera provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control. Accellera has developed seven standards that have been ratified by the IEEE. Accellera's recent successes in advanced design and verification language standards include SystemVerilog and PSL. For more information about Accellera, please visit www.accellera.org.

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Press Contact:
Georgia Marszalek, ValleyPR for Accellera, +650 345 7477, Georgia@ValleyPR.com

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