

Accellera Announces Call for Nominations for Its Technical Excellence Award Honoring Contributions to Its Design Automation Standards Efforts

Nominations are due June 9, 2006

Who/What

Accellera, the Electronic Design Automation (EDA) organization focused on language-based design standards, invites the electronic design community to nominate an individual for its 3rd annual Technical Excellence Award. Accellera's Technical Excellence Award recognizes an outstanding individual who has helped create electronic design standards that benefit the EDA industry.

When/Where

Nominations are due June 9, 2006. Please visit www.accellera.org to nominate an individual. The Award will be presented during Accellera's open meeting at the Design Automation Conference (DAC) in San Francisco.

About the Award and Accellera's Technical Subcommittees

Accellera's Technical Excellence Award recognizes the outstanding achievements of its technical committee members. Accellera will select a single contributor to the Accellera standards development process as its 2006 recipient. All Accellera Technical Subcommittee members are eligible for the award. A nomination form and more information are at www.accellera.org

Accellera's Technical Subcommittees include Formal Verification, Interface, Open Compression Interface (OCI), Open Verification Library (OVL), Property Specification Language (PSL), System Verilog, Verilog AMS and VHDL. For information about Accellera's Technical Subcommittees, please visit, www.accellera.org.

Accellera's Technical Subcommittees produce effective and efficient standards for today's advanced semiconductor designs. Participation comes from Accellera member companies, industry contributors and independents. Technical contributors typically have many years of practical experience developing and using design automation tools.

Information

For more information about Accellera and Accellera standards please visit www.accelera.org.

For more information about DAC, please visit www.dac.com.

About Accellera

Accellera provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control. For more information about Accellera, please visit www.accelera.org.

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