

**Accellera Invites DATE Attendees to Open Meeting,  
Open Verification Library Session,  
& Exhibition Theatre Panel on Electronic System Design Standards**

- *Open Meeting Covers Accellera's 2006 Standards Roadmap*
- *OVL Session Covers Usage*
- *Exhibition Theatre Panel Addresses Accellera's support for ESL Standards*

**Who**

Accellera, the Electronic Design Automation (EDA) organization focused on language-based design standards, invites the electronic design community to attend the 2006 Design Automation and Test in Europe (DATE) and to attend its open meetings: Open Industry Meeting, Open Verification Library (OVL) session, and a DATE Exhibition Theatre panel on European Support for ESL Standards.

**What:**

Open Industry Meeting

This session features a presentation about Accellera's Roadmap, as well as presentations from users highlighting their experience with adopting standards.

The Roadmap presentation includes plans for IEEE 1076™ VHDL and information about a new Open Compression Interface (OCI) standard that supports test-pattern generation and diagnosis for multiple on-chip scan structures.

Agenda:

Introduction: Rich Faris, Accellera Treasurer

Accellera 2006 Roadmap: Dennis Brophy, Accellera Vice Chairman

User presentations on successes and challenges of using IEEE 1800™ SystemVerilog, IEEE 1850™ Property Specification Language (PSL), and SystemVerilog Assertions (SVA)

Open Verification Library Session

This session highlights the Open Verification Library (OVL) standard as an integral part of an assertion-based verification strategy. The OVL standard creates a vendor- and language-

independent assertion library that can be leveraged across multiple verification processes. At this meeting, Mike Turpin, Chairman, and Kenneth Larsen, Co-Chairman of the OVL Technical Committee, will present on the revised Accellera OVL standard, which is a completely re-architected version of the original OVL that takes advantage of new features offered by the IEEE 1800™ SystemVerilog, IEEE 1850™ Property Specification Language (PSL).

#### European Support for ESL Standards Panel

Accellera Vice Chairman, Dennis Brophy, will talk about how Accellera supports the development of Electronic System-Level (ESL) standards and other organizations' efforts, as well as describe how Accellera interacts with different standardization bodies to improve electronic design productivity and quality.

#### **When/Where:**

##### Accellera Open Industry Meeting

Tuesday March 7, 2006 18:00-20:00

Room 02

##### Accellera Open Verification Library Session

Wednesday March 8, 10:00 – 10:55

Room 1427

##### European Support for ESL Standards Panel

Thursday March 9 1300 - 1400

Exhibition Theatre

ICM, MESSE, Munich, Germany

#### **Information & Registrations**

For more information about Accellera and Accellera standards and to reserve a spot at Accellera's open DATE meetings, please visit [www.accellera.org](http://www.accellera.org).

For more information about DATE, please visit [www.date-conference.org](http://www.date-conference.org)

## **About Accellera**

Accellera provides design and verification standards for quick availability and use in the electronics industry. The organization and its members cooperatively deliver much-needed EDA standards that lower the cost of designing commercial IC and EDA products. As a result of Accellera's partnership with the IEEE, Accellera standards are provided to the IEEE standards body for formalization and ongoing change control. For more information about Accellera, please visit [www.accellera.org](http://www.accellera.org).

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