



Join Us Today

Accellera Membership
Information

Accellera Wants You

- Influence the next generation of design languages and data formats
- Gain insight into evolving IC and system design methodologies



Accellera Beliefs

- Engineers and tools communicate through design languages and data formats
- These languages and formats must evolve to meet the changing needs of IC and system design
- Language and format standards enable interoperability and reduce development costs
- Standards should not be invented by committees, but should be adapted from industry
- Tool users and vendors must participate in standardization to maintain their competitive advantage



Accellera Process

- Identify needs for new language and format standards
- Solicit and accept donations from industry
- Drive refinement and standardization through technical working groups
- Submit standards to IEEE

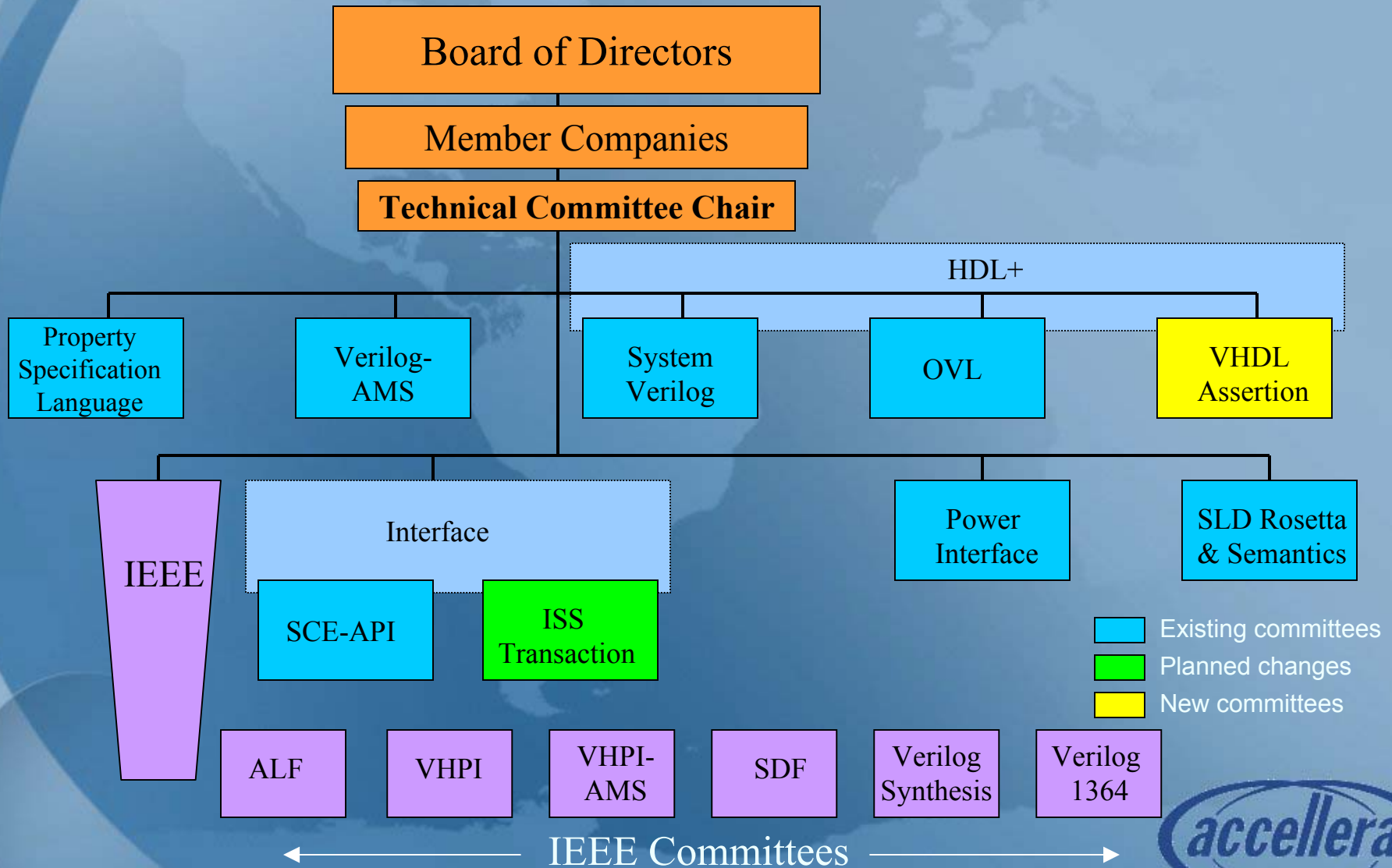


Accellera Benefits

- Insight
 - Understand the evolving requirements, tradeoffs, and development of languages and data formats
- Influence
 - Help determine the future of IC and system design methodology by working on language and data format standards



Accellera Organization



Accellera Accomplishments

- Verilog & VHDL Language Development & Adoption
- Formal Verification - Property Specification Language
- RTL Synthesis Interoperability Standards
- SystemVerilog
- Verilog Analog/Mixed Signal Language



Accellera Accomplishments

- IEEE 1497 Standard Delay Format
- IEEE 1076.4 VITAL (VHDL Initiative Towards ASIC Libraries)
- IEEE 1481 Delay & Power Calculation System
- IEEE 1499 Open Model Interface



Accellera Background

- Formed by merger of Open Verilog International and VHDL International in 2001
- OVI
 - Formed 1991, standardized Verilog
- VHDL International
 - Formed 1989, standardized VHDL



Accellera Members

- 0-In Design
- ARM
- Artisan
- Axis Systems
- Cadence
- IBM
- Intel
- JEITA
- Magma
- Mentor Graphics
- Motorola
- National Semiconductor
- NEC
- Novas
- NVIDIA
- Real Intent
- Silvaco
- ST Microelectronics
- Sun Microsystems
- Synopsys
- TNI-Valiosys
- Toshiba
- Verisity

... and over 4,000 Designers Forum members



Join Now

- Board of Directors are elected annually during DAC
- Membership year is April 1 to March 31
- Contact Accellera office for membership application or go to www.accelera.org
 - Call (707) 251-9977 or e-mail lynnh@accelera.org

