Section **5.2, A.2.5**
Abbreviated packed dimension

In A.2.5, in packed_dimension, REPLACE

        [constant_range **]**

WITH

        [constant_range **]**

   | **[** constant_expression **]**

[Note to editor: the square brackets **[ ]** should be **red**.]

In 5.2, REPLACE

SystemVerilog accepts a single positive number, as an alternative to a range, to specify the size of an unpacked array,
like C. In other words, `[size]` becomes the same as `[0:size-1]`. For example:

    `int Array[8][32]; is the same as: int Array[0:7][0:31];`

WITH

SystemVerilog accepts a single positive number, as an alternative to a range, to specify the size of a packed or unpacked dimension. If the dimension is packed, then `[size]` becomes the same as `[size-1:0]`. If the dimension is ~~an~~ unpacked ~~array~~, ~~like~~ then, as in C, ~~. In other words,~~ `[size]` becomes the same as `[0:size-1]`. For example:

    `~~int~~ var [16]Array[8][32]; is the same as: ~~int~~ var [15:0]Array[0:7][0:31];`