

# System Verilog

25-Apr-03

Summary of votes to send LRM to the Board

	Cadence	IBM	IEEE	Intel	Mentor	Motorola	Novas	Sun	Synopsys	0-In	Total	
SV-AC	Approve		*	1	1	1	1	1	1		6	66.67%
	Abstain		1	*						1	2	22.22%
	Against	1		*							1	11.11%
SV-BC	Approve		*	*	1	*	*	*	1	*	2	66.67%
	Abstain		*	*		*	*	*		*	0	0.00%
	Against	1	*	*		*	*	*		*	1	33.33%
SV-CC	Approve		*	*	*	1	1	1	*	1	4	80.00%
	Abstain		*	*	*			*		*	0	0.00%
	Against	1	*	*	*			*		*	1	20.00%
SV-EC	Approve		*	1	*	1	1	*	1	1	5	83.33%
	Abstain		*		*			*		*	0	0.00%
	Against	1	*		*			*		*	1	16.67%
Total	Approve			1	2	3	3	2	2	4	17	73.91%
	Abstain		1							1	2	8.70%
	Against	4									4	17.39%
By Company	Approve			1	1	1	1	1	1		7	70.00%
	Abstain		1							1	2	20.00%
	Against	1									1	10.00%

By all measures the LRM has been approved to send to the board by at least 2/3  
 \* means that the company was not eligible to vote in the committee due to lack of attendance