

SystemVerilog Basic Committee



Johnny Srouji (Chair)
Karen Pieper (Co-Chair)

- Charter
 - To address issues exposed in the SystemVerilog 3.0 standard through implementation without removing any functionality from the language
- Who are we
 - 55 members of the mailing list
 - Regular attendees:

Cliff Cummings (Sunburst Design)

Steven Sharp (Cadence)

Gord Vreugdenhil (Synopsys)

Don Mills (LCDM Engineering)

David Smith (Synopsys)

Dennis Brophy (Model Technology)

Brad Pierce (Synopsys)

Mike McNamara (Verisity)

Kevin Cameron (NSC)

Tom Fitzpatrick (Co-Design)

Francoise Martinolle (Cadence)

Dan Jacobi (Intel)

- We had a successful face-to-face meeting
 - Peter was able to clarify a number of issues
 - All of the issues have owners developing proposals
- Topics covered
 - Interfaces
 - Logic type semantics
 - Typedef usage and syntax
 - Time data types
 - Always_comb semantics
 - Auto-increment semantics

- 172 issues have been filed
- Broken down:
 - 78 (45%) issues have been addressed
 - 23 (13%) issues are simple BNF issues that are not addressed
 - 15 (9%) issues are actively being addressed by SV-BC
 - 16 (9%) issues are being considered by IEEE 1364
- We expect a number of new issues to be filed by Steven Sharp this month

SV-BC: Schedule and deliverables

- Meeting schedules
 - Every two weeks for 2 hours
 - All day meeting being scheduled in January
 - > We are hoping Peter will be able to join us
- Deliverables
 - Updated standard addressing known issues by February 28
- Schedule
 - All issues must be filed by December 31
 - 80% of issues addressed by February 10
 - Standard update for initial voting February 28