



Accellera Portable Test and Stimulus Standard

Release Notes for 1.0

1. Added escaped identifiers (4.3)
2. Clarified syntax for **string** data type (8.4)
3. Clarified **activity** evaluation with extension and inheritance (11.2)
4. Changed **repeat-while** to just **while** (11.4.2)
5. Added optional iterator variable to **foreach** statement and constraint (11.5.3, 15.1.7)
6. Added weights and guard expressions for **select** statement (11.5.4)
7. Added the **match** statement, similar to case/switch (11.5.6)
8. Changed **symbol** syntax to be consistent with other constructs [i.e. removed "="] (11.6.1)
9. Added ability of flow and resource objects to have sub-fields of the same type (12, 13)
10. Changed built-in attributes in C++ to be accessed directly, similar to user-defined attributes, instead of having a built-in accessor method (13.1.2)
11. Restructured Coverage constructs to be more SystemVerilog-like (17)
12. Clarified and extended **package** declaration (19.1)
13. Added **exec** block evaluation with extension and inheritance, including **super**. (20.2)
14. Added Referencing PSS fields in target-template exec blocks (20.3)
15. Added Conditional Code Processing (Clause 21)
16. Removed Hardware/Software Interface Clause and Annex F
For verification of a given *target platform* (for example, a Silicon-on-Chip or an ASIC), software routines that can initialize, configure and operate the underlying hardware will need to be provided by the user, and can be specified via the procedural interface as defined in 20.4.